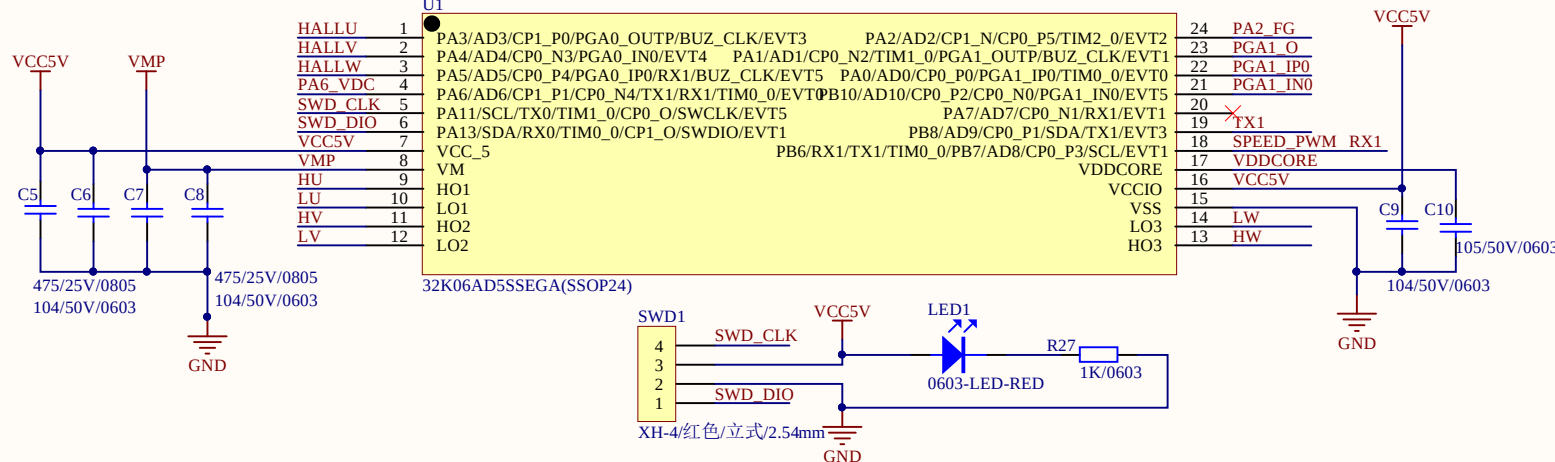
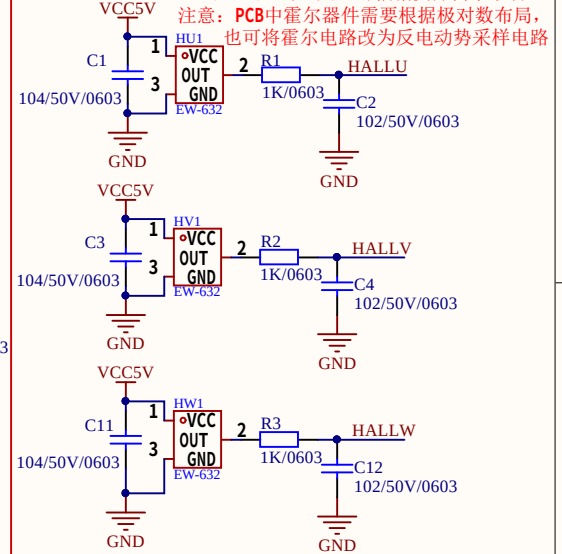


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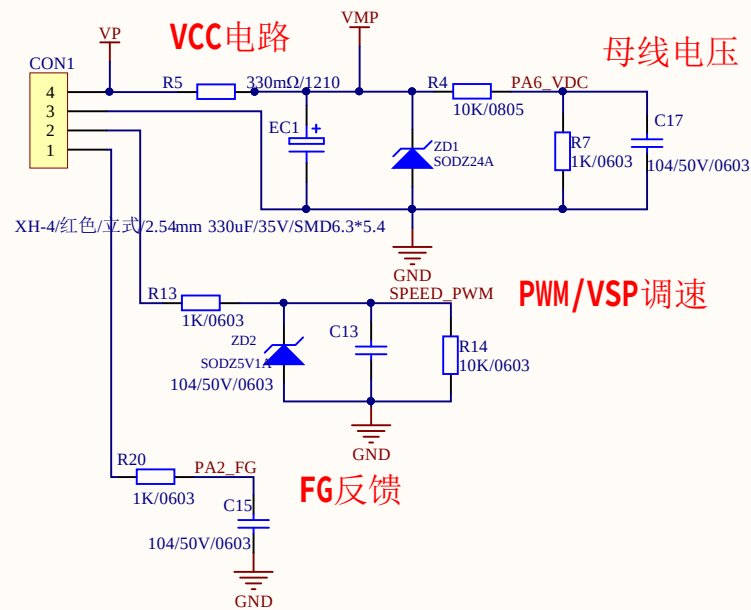
低压单电阻采样+三霍尔方案



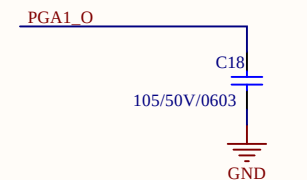
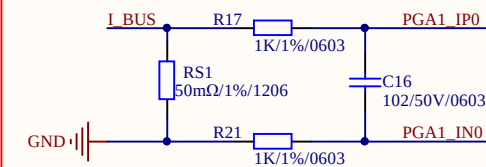
霍尔采样(可根据实际需求取舍)



接线端子

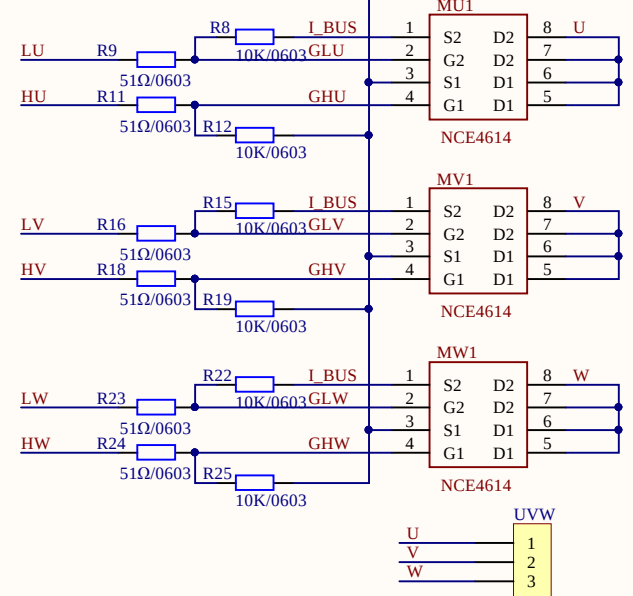


单电阻采样+恒功率



功率电路

MOS器件根据实际需求替换,但注意L/H 接线位置



XH-3/白色/立式/2.54mm

如果对EMI有较高的要求,建议在UVW输出端预留串接共模电感。

霍尔根据实际极对数布局
此处布局仅供参考

